

TTC_DB5

BIT SYNCHRONIZATION AND DATA DECOMMUTATION



DESCRIPTION

The **TTC_DB5** combines the functions of bit synchronization and data decommutation into a single low cost PCMCIA type II card. The card can be installed in a portable PC compatible platform for pre-flight or lab test.

The bit synchronizer provides full featured clock reconstruction, data recovery and code conversion. The bit sync accepts PCM inputs at rates of up to 20 Mbps for NRZ codes and up to 10 Mbps for BiØ codes. The bit sync output is also internally connected to the on-card decom.

The data decommutator provides full IRIG frame synchronization and data decommutation. The decom accepts PCM data at rates up to 20 Mbps from the on-card bit sync or an external source.

KEY FEATURES

- **PCMCIA-based bit synchronizer/data decommutator**
- Bit rates **up to 20 Mbps, NRZ codes**
- Accepts all IRIG 106 PCM inputs
- Provides all **IRIG 106 PCM** data outputs
- On-board bit error rate detector and test data simulator
- PCM input rate up to 20 Mbps
- Accepts **RS-422, or TTL** input data and clock

SPECIFICATIONS

Bit Synchronizer

PCM Inputs	One single ended (analog) and one RS-422 differential, programmable
Input Codes	NRZ-L/M/S, RNRZ-L (fwd/rev) or BiF -L/M/S per IRIG- 106-01, programmable. Additional codes DM-M/S
Analog Input level	0.1 to 5.0 VP-P
Analog Input Termination	Programmable at 50 ohms, 75 ohms or 10 kohms
Bit Rate	NRZ Codes : Up to 20 Mbps BiF Codes Up to 10 Mbps DM, MDM Codes : Up to 10 Mbps
Loop Bandwidth	0.1% to 1.6% of bit rate, programmable

Decommutator

PCM data	NRZ-L data input.
Polarity	Programmable for normal (0 degrees), or inverse (180 degrees)
Impedance	TTL 75 ohms or TTL 10 kohms, programmable for clock and data
Level	TTL or RS-422 programmable for clock and data
Rate	1 Bps to 20 Mbps
Sync Pattern	Up to 32 bits, programmable
Minor Frame Length	Up to 1024 words per minor frame
Major Frame length	Up to 256 minor frames per major frame
Major Frame Synch	SFID and sync bits

ORDERING INFORMATION

TTC_DB	Standard device
TTC_DB/2	Including DM-M/S and MDM-M/S codes available at Bit sync input and Simulator output

*Specifications are subject to change.
Please, verify the latest specifications
prior order.*

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